Design and Simulation of printed micro strip low pass filter based on the Electromagnetic Models 18GHz printed microstrip lowpass filter using X-Models

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Abstract—this paper presents the Design, Simulation and characterization of Printed Micro strip Low Pass Filter based on 15 mil Alumina substrate. Simulations show a comparison between a pure EM simulation with EM Sight and a more efficient hybrid approach that combines EM analysis (using X-Models) with conventional circuit simulations and both approaches match the measured data. Furthermore, the proposed paper has four different low pass filter designs, lumped design; basic designs uses only micro strip lines; advanced design uses multiple edge coupled micro strip lines, EM sight design provides a very sharp cut-off frequency response with low insertion loss, results having great agreement with excellent correlation. Tuning and Optimization of the design is carried out using AWR Microwave office tool.

Keywords— EM sight, AWR, MMIC, X-Models, I-Filter

I. INTRODUCTION

A low-pass filter (LPF) offers easy passage to low-frequency signals and difficult passage to high-frequency signals. It is highly demanded in modern microwave communication systems, especially in wireless and mobile communications in order to suppress harmonics and spurious signals [1]. The amount of insertion loss and phase shift encountered by a signal passing through the filter and the amount of rejection of an undesired signal is a function of the filter design. Filters are networks that process signals in a frequency-dependent manner and they can be explained by examining the frequency dependent nature of the impedance of capacitors and inductors. With the advent of printed-circuit board (PCB) technology, microwave system also develop a strip line components and system with a technique of integrated circuit, called MMIC. It is a type of Integrated circuit device that operates at microwave frequencies (300MHz - 300GHz). Inputs and outputs on MMIC devices are frequently matched to a characteristic impedance of 50 ohms. In advanced filter design, micro strip edge-coupled filters are used, they have advantages such as easy design procedures and simple integration into circuits. A general structure of parallel-coupled (or edge-coupled) micro strip filters that use half-wavelength line resonators. They are positioned so that adjacent resonators are parallel to each other along half of their length. This parallel arrangement gives relatively large coupling for a given spacing between resonators, therefore this filter structure is particularly convenient for constructing filters having a wider bandwidth as compared to the structure for the end coupled microstrip filters. For the creation of EM waves we need specific structures with time-varying charges and currents. At high frequencies hollow waveguides are used to transmit energy because they do not radiate at any frequency. There are new design methodologies for EM analysis such as topological partitioning, functional partitioning, and tuning methodology. In this filter design topological partitioning method is applied since it has advantage of gain in speed and also one can make certain changes very quickly.

This paper deals with the designing and fabrication of microstrip low pass filters. The designing is done using AWR Microwave Office simulation program developed by Applied Wave Research USA. This is a user-friendly software with all of the capabilities necessary for the accurate modelling and design of microwave components. It contains a linear, harmonic-balance, time domain, EM simulation, physical layout and includes linear and nonlinear noise analysis and can model nonlinear behavior existing in microwave devices.
II. I-FILTER

I-Filter is specifically developed for synthesis of lumped element and distributed filters, it brings useful filter synthesis to the integrated desktop. The module plugs directly into the Microwave Office design environment and is integrated as a wizard within the AWR Design Environment (AWRDE), it is completely integrated into AWRDE and easily accessible. Limits for minimum line width and minimum line spacing are specified in iFilter, which are typical dimensions for modern printed circuit board processes.

I-Filter also provides the ability to adjust secondary parameters in real time and see the results, in the case of hairpin filters, includes adjustment of the filter’s nominal impedance. The short connecting lines between the resonators (in the U-turns), will have this impedance and the resonator lines will share this line width. If the area is assumed to be limited to 0.36 x 0.30 inches, the nominal impedance of the resonators can be adjusted to examine the effects of line width on performance and required area. Additionally, I-Filter performs accurate synthesis through electromagnetic verification with for instance, AWR’s AXIEM 3D planar EM simulator. This is especially important when designing distributed filters.

![Fig. 1. Schematic showing ifilter design](image)

A. FEATURES:
- Simplicity.
- Power – ideal or real, lumped or distributed, wide variety
- Ease-of-use.
- Integrated – works directly with AWR layout, EM, and optimization.
- Upgradeable – options for your design needs.

III. MICROSTRIP

Microstrip line consists of thin strip conductor suspended over ground plane by a low-loss dielectric material. Waves traveling in microstrip line not only travels in the dielectric medium they also travel in the air media above the microstrip line. Thus they don’t support pure TEM waves, rather it supports quasi TEM approximation.

![Fig. 2. A basic Microstrip structure](image)

The effective dielectric constant in terms of $W$ (width of the Microstrip), $h$ (height of the substrate) and $\varepsilon_r$ (relative dielectric constant) given by Hammerstad and Jensen [11] is:

$$\varepsilon_{re} = \frac{\varepsilon_r + 1}{2} + \frac{\varepsilon_r - 1}{2} \left( \frac{1}{1 + \frac{12h}{W}} \right)$$  \hspace{1cm} (1)

Characteristic impedance of the microstrip line is given by

$$Z_c = \frac{1}{c\sqrt{\varepsilon_{re}}}$$  \hspace{1cm} (2)

Where $c$ is the velocity of electromagnetic waves in free space $c=2.99\times10^8$ m/s. The accuracy of $Z_c\sqrt{\varepsilon_{re}}$ is better than 0.01 %.

IV. INITIAL LUMPED DESIGN:

Insertion loss method is used in designing lumped element filter. It provides ways to shape pass band and stop band of the filter, although its design theory is much more complex. The order $N$ of a filter can be found as follows:

$$N = \frac{\cosh^{-1}\sqrt{(10^{0.1\times IL} - 1)/(10^{0.1\times \alpha} - 1)}}{\cosh^{-1}(\omega/\omega_c)}$$  \hspace{1cm} (3)

The filter design specification has cut-off frequency of 18GHz. From the above equation order of the filter is found to be 7. For this design prototype parameters are as follows:

$$g_0 = g_8 = 1$$
$$g_1 = g_7 = 1.3722$$
$$g_2 = g_6 = 1.3781$$
$$g_3 = g_5 = 2.2756$$
$$g_4 = 1.5001$$

V. IMPLEMENTATION:

B. Basic design:

Basic filter is designed using microstrip lines such as MLIN, MLEF and MTEES microstrips which are available in AWR tool. Input/Output ports are matched to 50ohms, these are also available in AWR tool. Microstrip has relative dielectric constant, $\varepsilon_r=9.8$, substrate thickness $H=15\text{mil}$, conductor thickness $T=0.3\text{mil}$, metal bulk resistivity normalized to gold $\rho=2$ and loss tangent dielectric, $\tan\delta=0.002$. Basic filter design has insertion-
loss of -1.0896dB and return loss of -42.441dB at 18.95GHz.

C. Advanced filter design:
Advanced filter is designed using edge coupled microstrip line. The following figure shows the cross section of a coupled line. They support two modes of excitation, even and odd mode.

Fig. 4. A Coupled Line Structure.

D. Even mode:
In even mode excitation both the microstrip coupled lines have the same voltage potential resulting in a magnetic wall at the symmetry plane.

Fig. 5. Quasi-TEM, Even Mode of a Pair of Coupled Microstrip Lines

The Odd mode Impedances can be calculated using the following formulae.

\[(Z_{oe})_{j,j+1} = \frac{1}{Y_0} \left[1 + \frac{I_{0j}}{Y_0} + \left(\frac{I_{0j}}{Y_0}\right)^2\right]\]  (4)

Where,

\[\frac{I_{0j}}{Y_0} = \frac{\pi\Delta}{2g_0g_1}\]  (5)

\[\frac{I_{jj+1}}{Y_0}_{j=1 to n-1} = \frac{\pi\Delta}{2\omega_1 \sqrt{2g_0g_{j+1}}}\]  (6)

\[\frac{I_{jn,n+1}}{Y_0} = \frac{\pi\Delta}{2g_0g_{n+1}}\]  (7)

E. Odd Mode
In odd mode the coupled microstrip line possess opposite potential. This results into an electric wall at the symmetry. The following cross section diagram shows the same.

Fig. 6. Quasi-TEM, Odd Mode of a Pair of Coupled Microstrip Lines

The Odd mode impedances can be calculated as same as Even mode impedances using the following formulae.

\[(Z_{oo})_{j,j+1} = \frac{1}{Y_0} \left[1 - \frac{I_{0j}}{Y_0} + \left(\frac{I_{0j}}{Y_0}\right)^2\right]\]  (8)

Where,

\[\frac{I_{0j}}{Y_0} = \frac{\pi\Delta}{2g_0g_1}\]  (9)

\[\frac{I_{jj+1}}{Y_0}_{j=1 to n-1} = \frac{\pi\Delta}{2\omega_1 \sqrt{2g_0g_{j+1}}}\]  (10)

\[\frac{I_{jn,n+1}}{Y_0} = \frac{\pi\Delta}{2g_0g_{n+1}}\]  (11)

TABLE I. ODD AND EVEN IMPEDANCES VALUES OBTAINED FROM THE ADMITTANCE INVERTER

<table>
<thead>
<tr>
<th>J</th>
<th>Even – mode impedance ((Z_{oe})_{j,j+1})</th>
<th>Odd – mode impedance ((Z_{oo})_{j,j+1})</th>
<th>Characteristic Impedance (Z_0)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>78.777</td>
<td>37.9177</td>
<td>54.6514</td>
</tr>
<tr>
<td>1</td>
<td>59.7244</td>
<td>43.0544</td>
<td>50.7089</td>
</tr>
<tr>
<td>2</td>
<td>57.3072</td>
<td>44.3672</td>
<td>50.4238</td>
</tr>
<tr>
<td>3</td>
<td>56.9688</td>
<td>44.5688</td>
<td>50.3888</td>
</tr>
</tbody>
</table>

Applied Wave Research has developed accurate advanced numerical models for microstrip edge coupled lines, which are labelled MXCLIN elements. “X,” which can range from 3 to 16, represents the number of parallel edge-coupled microstrip lines. Figure.7 shows AWR MXCLIN with 8 coupled lines.
F. Electromagnetic model:

At frequency greater than 3GHz it is compulsory for any microstrip design to undergo EM simulation. EM simulation considers all the dielectric effect so the response of the schematic will lose some of its characteristics and as the frequency goes higher and higher these responses may vary enormously. So at the higher frequency the EM Simulation is very necessary simultaneously it is difficult to meet the specification. To increase the length of all resonators by connecting a multiple coupled line in between the two halves. The multiple coupled lines can come from a good circuit theory model, or it can be generated from an EM analysis. Increase the length of the added line to decrease the center frequency of the filter. To shorten all the resonators, connect a negative length line. While not physical, circuit theory and EM analysis programs both have no trouble doing this just connect a multiple coupled line in between the two halves. No need to repeat the entire EM analysis. Tune up your layout with circuit theory, do one more EM analysis to confirm the changes, and then fabricate. Design closure, quick and easy: a tunable EM analysis.

VI. RESULT AND ANALYSIS:

Insertion loss plot of lumped element low pass filter is shown in figure 10. Here the insertion loss at 18.14GHz is -2.999dB.

Fig. 10. Insertion loss of Lumped design.

Return loss of lumped element low pass filter is shown in fig 11. Here the return loss at 18.011GHz is -16.029dB.

Fig. 11. Return loss of Lumped design

Insertion loss of Basic low pass filter design is shown in fig 12. Here the insertion loss at 18.95GHz is -1.0869dB.

Fig. 12. Insertion loss of Basic low pass filter.

Return loss of basic low pass filter is shown in fig 13. Here the return loss at 18GHz is -42.441dB.
Insertion loss of advanced low pass filter is shown in fig 14. Here the insertion loss at 18.498GHz is -3.019dB.

Return loss of advanced low pass filter is shown in fig 15. Here the return loss at 18GHz is -10.001dB.

Insertion loss of EM sight low pass filter is shown in fig 16. Here the insertion loss at 18.586GHz is -3.039dB.

Return loss of EM sight low pass filter is shown in fig 17. Here the return loss at 18.124GHz is -20dB.

VSWR comparison between iFilter, basic LPF, advanced LPF and EM sight design is shown in fig 18. The advanced design and EM sight design having very good VSWR value. Variation of VSWR curve is crazy in Lumped design which is shown in fig 19.

VII. SUMMARY:

<table>
<thead>
<tr>
<th>Design</th>
<th>Frequency</th>
<th>Insertion loss</th>
<th>Return loss</th>
<th>VSWR</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lumped design</td>
<td>18.14GHz</td>
<td>-2.996dB</td>
<td>-16.067dB</td>
<td>1.5</td>
</tr>
<tr>
<td>Basic design</td>
<td>18.95GHz</td>
<td>-1.0869dB</td>
<td>-42.441dB</td>
<td>1.029</td>
</tr>
<tr>
<td>Advanced design</td>
<td>18.498GHz</td>
<td>-3.0195dB</td>
<td>-10.001dB</td>
<td>1.826</td>
</tr>
<tr>
<td>EM sight design</td>
<td>18.586GHz</td>
<td>-3.039dB</td>
<td>-20.001dB</td>
<td>1.081</td>
</tr>
</tbody>
</table>
VIII. CONCLUSION:

This paper shows the design of 18GHz printed microstrip low pass filter on 15 mil Alumina substrate and simulation shows the comparison between lumped, basic, advanced and EM sight design successfully. The "LPF EMSight" shows the filter in the EMSight 2.5D electromagnetic simulator, which is viewed in three dimensions. This compares very closely to measured data. The "LPF Advanced" schematic makes use of advanced X-Models (EM-based models), coupled lines and other linear schematic elements, including a substrate definition. This too gives excellent correlation. The "LPF Basic" indicates potential errors in schematic entry with the associated errors in results. A good comparison is achieved among all four designs and validated through circuits and EM sight in AWR MWO Environment, furthermore particular design can be chosen based on tradeoffs.

The design will be useful for Satellite and PTP Communications Links, Marine and Pleasure Craft Radar, Port Vessel Traffic Services etc…

TABLE III. PARAMETERS COMPARISON WITH PREVIOUS DESIGNS

<table>
<thead>
<tr>
<th>References</th>
<th>[2]</th>
<th>[7]</th>
<th>[10]</th>
<th>This work</th>
</tr>
</thead>
<tbody>
<tr>
<td>Order of the filter</td>
<td>5</td>
<td>4</td>
<td>3</td>
<td>7</td>
</tr>
<tr>
<td>Cut-off frequency</td>
<td>2 GHz &amp; 3GHz</td>
<td>7.7GHz</td>
<td>1990MHz</td>
<td>18.95GHz</td>
</tr>
<tr>
<td>Insertion-loss</td>
<td>-0.6dB</td>
<td>18dB</td>
<td>&lt;0.6dB</td>
<td>-1.0869dB</td>
</tr>
</tbody>
</table>

REFERENCES:


[7] “A DC to 6 GHz Balanced Elliptic Low-Pass Filter in CMOS 130nm Technology”, by Mohammad S. Mahani, Ramesh Abhari Electrical and Computer Engineering Department, McGill University, Montreal, Quebec, Canada 978-1-4577-1318-7/12/$26.00 © 2012 IEEE.


